

DELAY CIRCUIT WITH DELAY RELATIVELY INDEPENDENT OF
PROCESS, VOLTAGE, AND TEMPERATURE VARIATIONS

ABSTRACT OF THE DISCLOSURE

Methods and systems for controlling delay relatively independent of process, supply-voltage, and/or temperature ("PVT") variations include sensing an output signal after a number of inverters and activating different numbers of transistors and/or adjusting strength of transistors in a delay path to compensate for PVT variations. In an embodiment, a waveform is received, delayed, and output to an output terminal using at least one relatively low-power device. Supplemental output power is provided by at least one relatively high-power device until the output waveform exceeds a threshold.

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